

FIG. 1A

VI ARCHITECTURAL MODEL

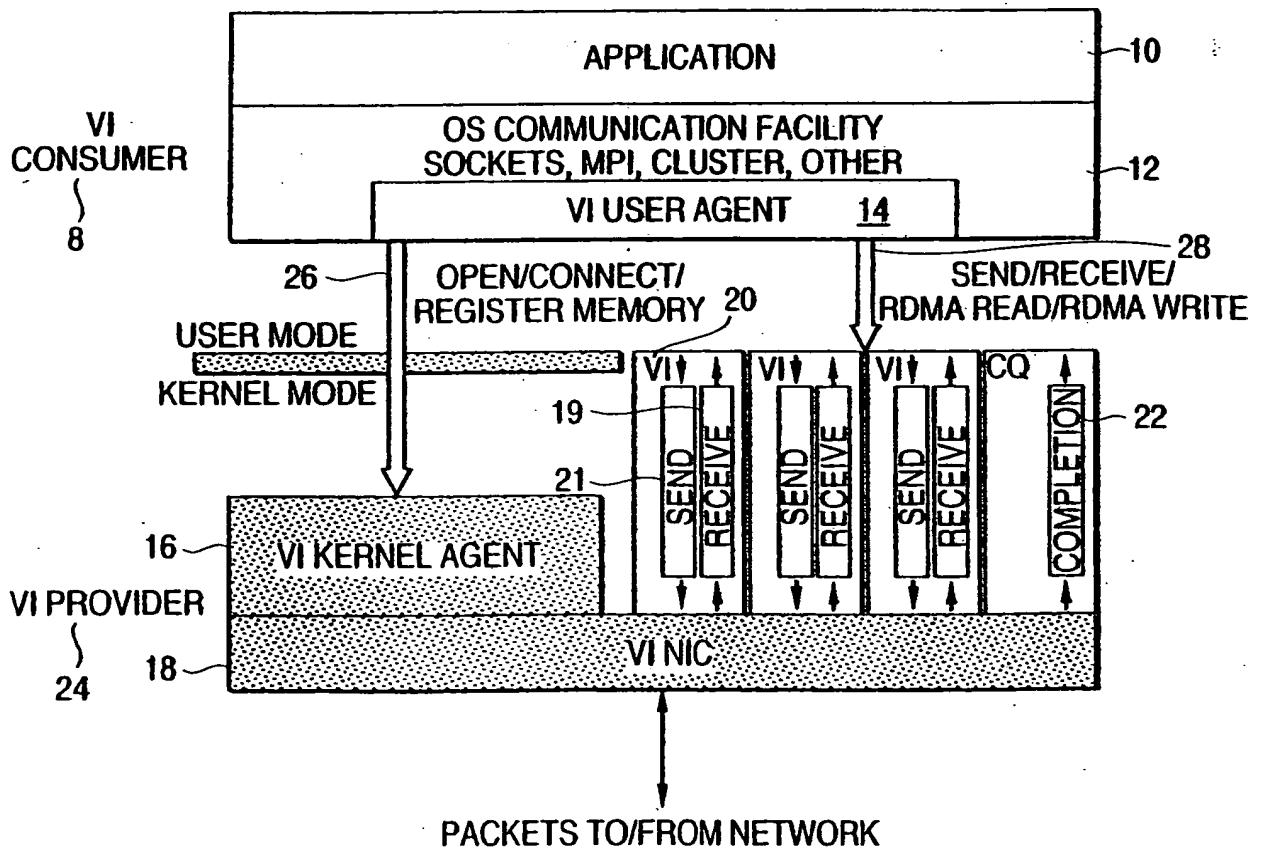
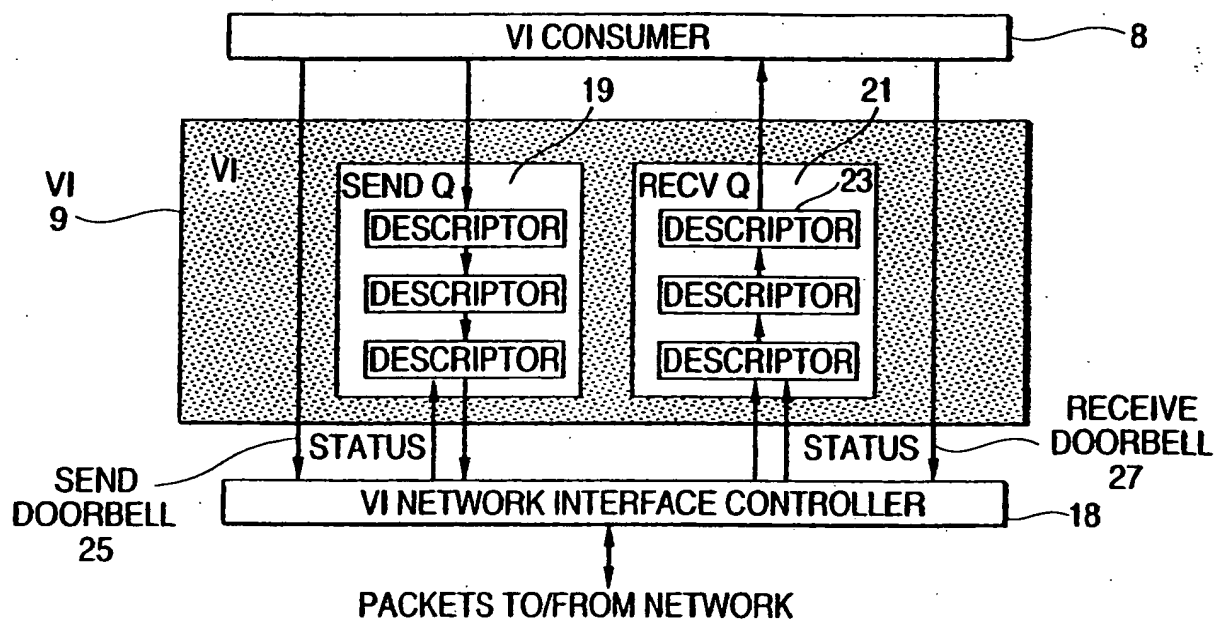


FIG. 1B



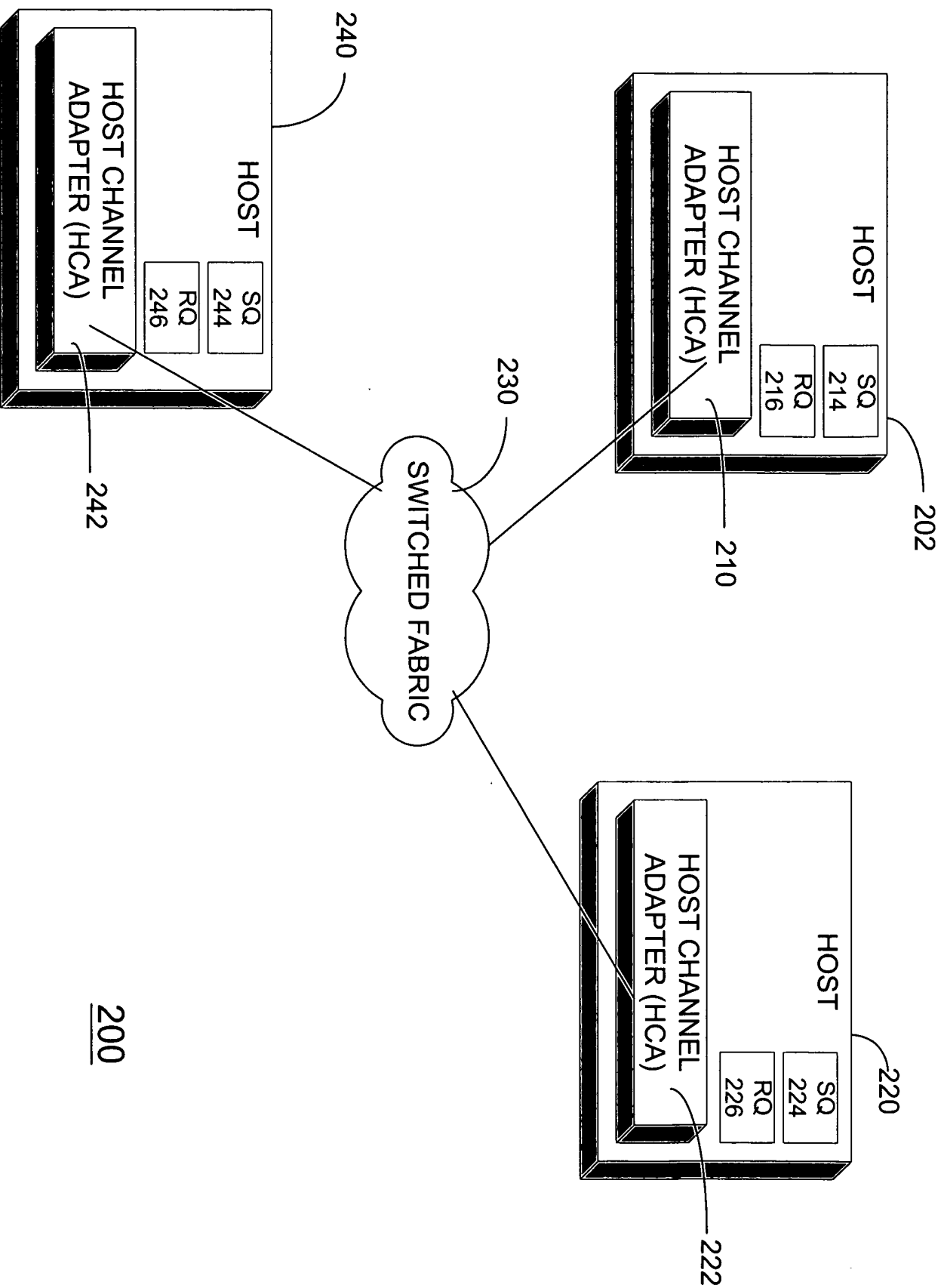
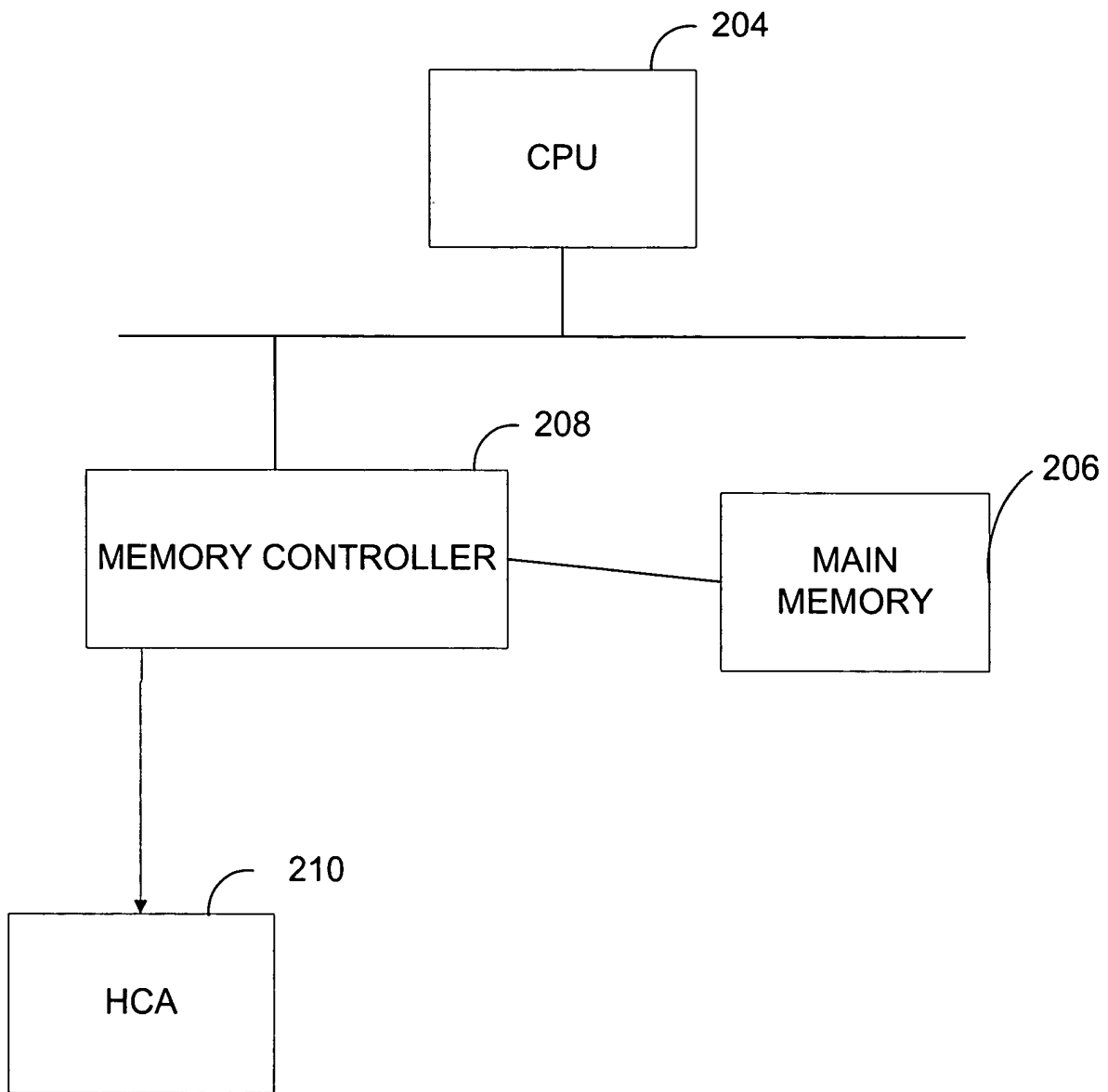


FIG. 2



HOST 202

FIG. 3

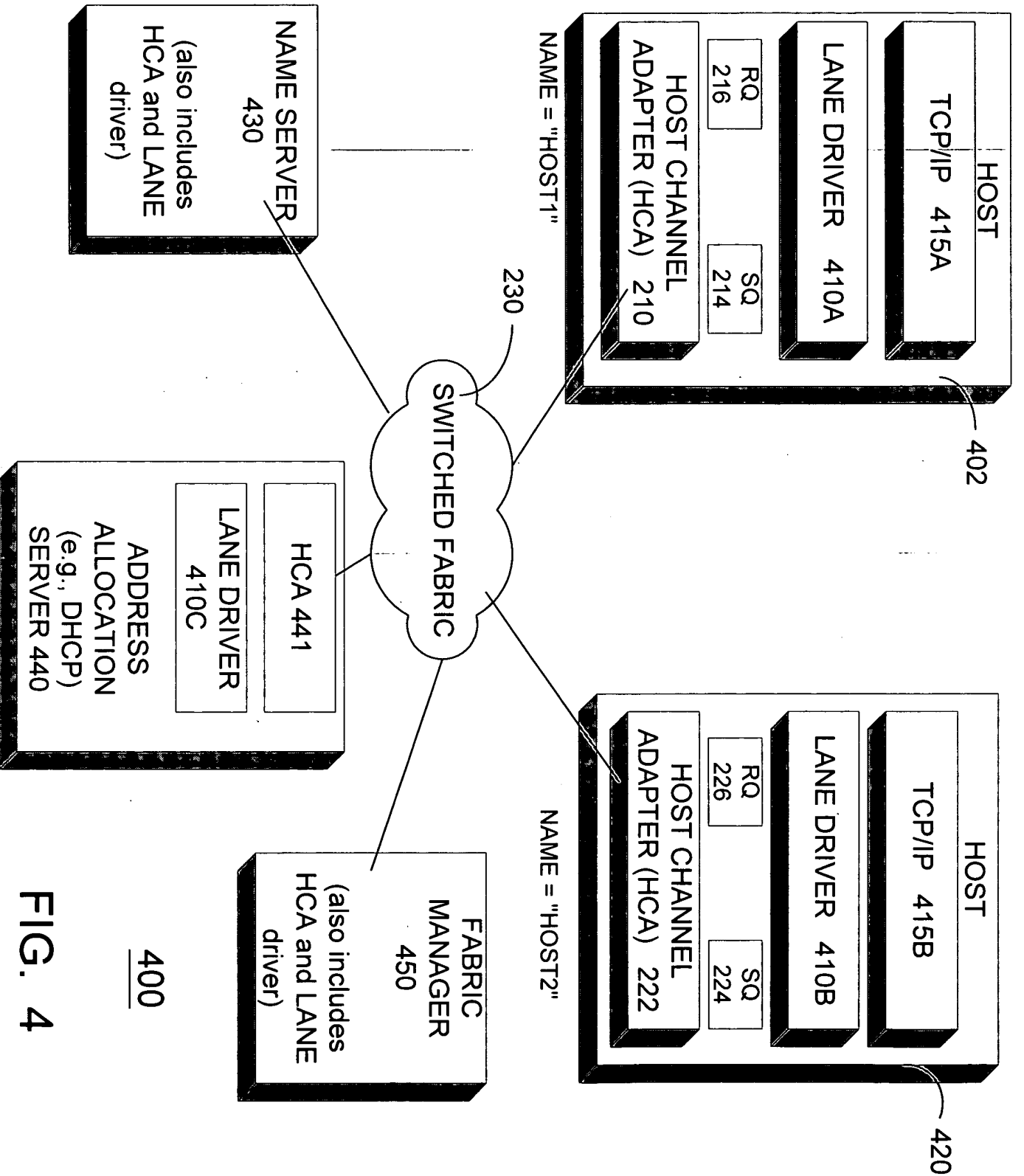


FIG. 4

400

FIG. 4 is a block diagram of a network architecture 400. The architecture 400 includes a switched fabric 230. The switched fabric 230 is connected to a host 402 and a host 420. The host 402 includes a TCP/IP stack 415A, a lane driver 410A, and a host channel adapter (HCA) 210. The host 420 includes a TCP/IP stack 415B, a lane driver 410B, and a host channel adapter (HCA) 222. The host channel adapters 210 and 222 are connected to the switched fabric 230 via request queues (RQ) 216 and 226, and service queues (SQ) 214 and 224. The switched fabric 230 is also connected to a name server 430, an address allocation server 440, and a fabric manager 450. The name server 430, address allocation server 440, and fabric manager 450 all include HCA and LANE drivers.

GLOBAL PHYSICAL
ADDRESS FOR HOST/
NODE (e.g., IEEE 802.3
ETHERNET MAC
ADDRESS (48 BITS))

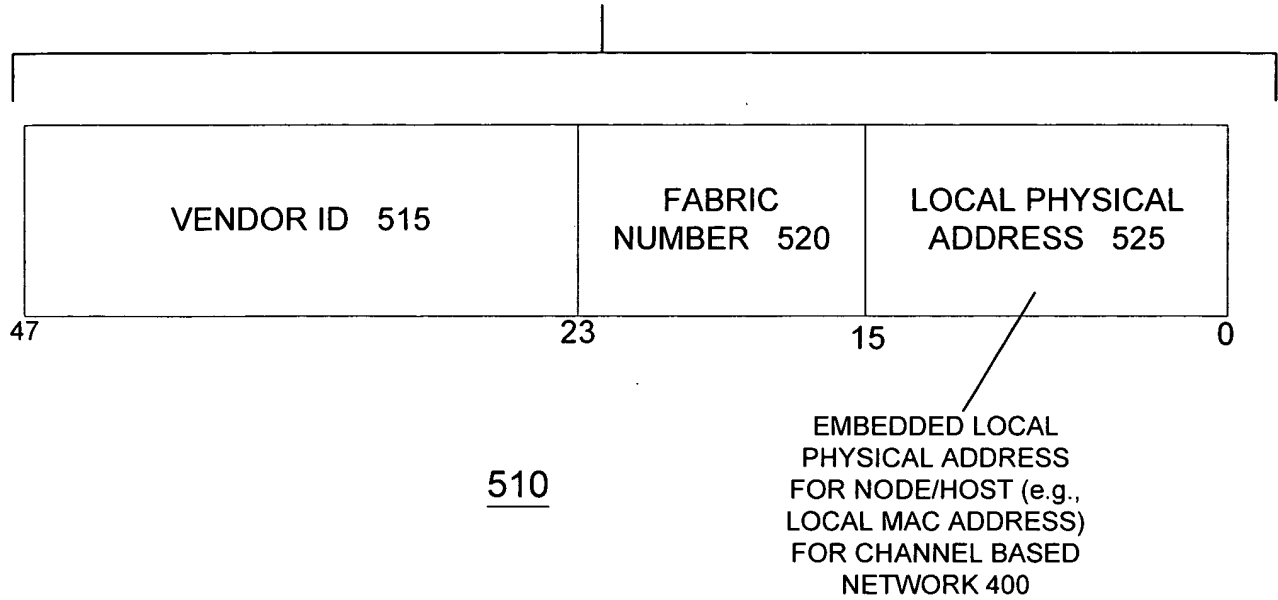


FIG. 5

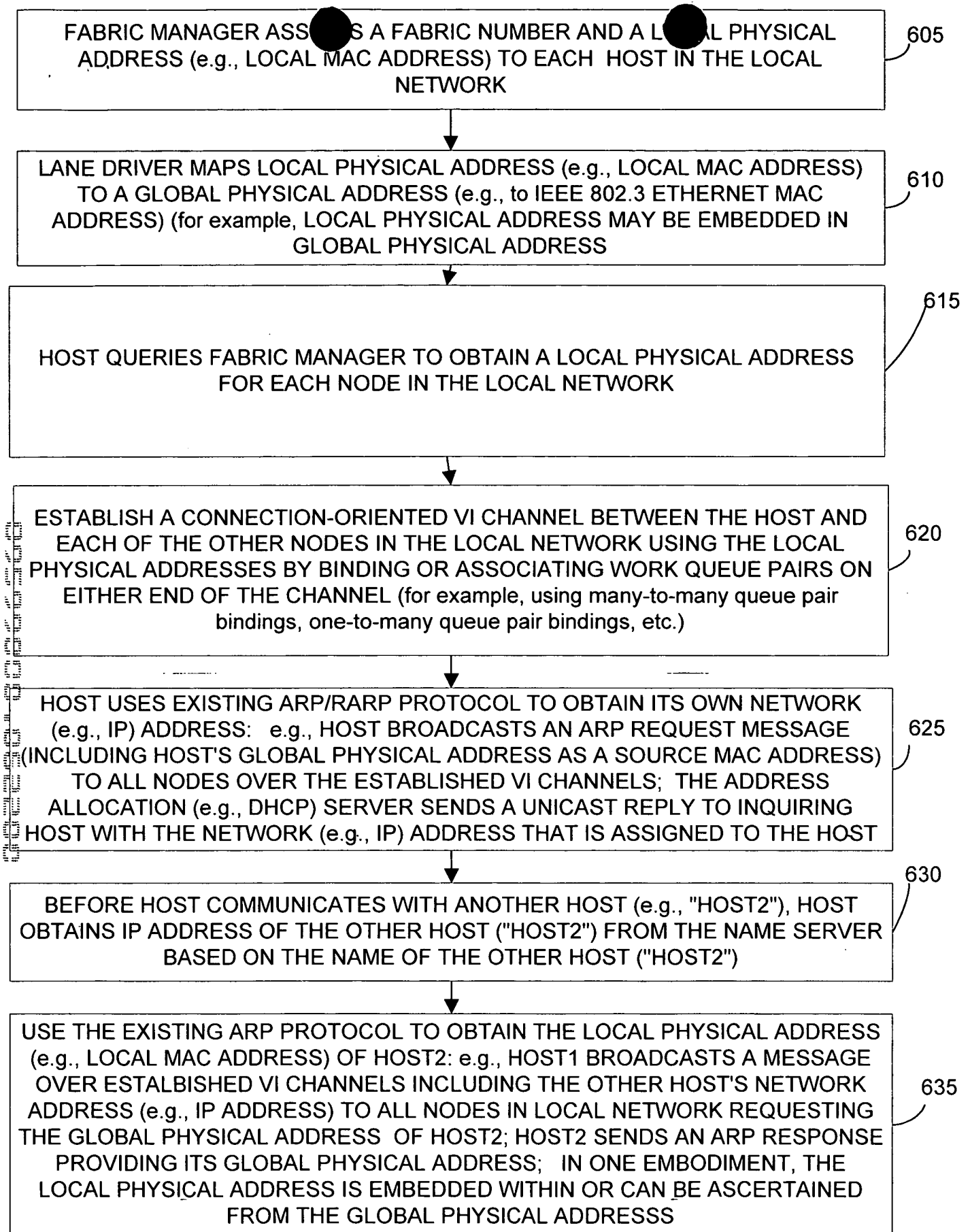


FIG. 6